



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

STEPHEN H. GUNTHER, ET AL.

Application No.: 10/821,822

Filed: April 9, 2004

For: **METHODS AND APPARATUS FOR THERMAL
MANAGEMENT OF AN INTEGRATED CIRCUIT
DIE**

Art Group: 2863

Examiner: Tung S. Lau

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
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In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No. 09/784,255, filed February 14, 2001) and copies of the references were furnished at that time. Accordingly, per 37 CFR §1.98(d)(1) additional copies of those references are not submitted herewith.

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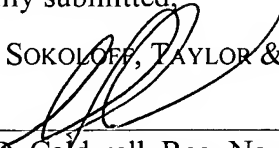
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The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Date: February 24, 2006



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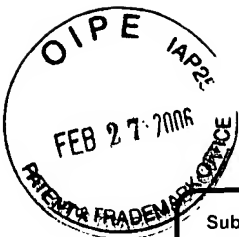
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/821,822
				Filing Date	April 9, 2004
				First Named Inventor	Stephen H. Gunther
				Art Unit	2863
				Examiner Name	Tung S. Lau
				Attorney Docket Number	42P4728XC
Sheet	1	of	1		

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
		SHU-YUAN CHIN ET AL., "A New Type of Curvature-Compensated CMOS Bandgap Voltage References", <i>VLSITSA</i> , 1991, 6 pages, IEEE Catalog No. 91TH0368-1, ISBN 0-7803-0035-X.		
		MARCO FERRO ET AL., "A Floating CMOS Bandgap Voltage Reference for Differential Applications", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 24, No. 3, June 1989, pg. 690-697, ISSN 0018-9200.		
		O. SALMINEN ET AL., "The Higher Order Temperature Compensation of Bandgap Voltage References", <i>IEEE International Symposium on Circuits and Systems</i> , Vol. 3, San Diego, California, May 10-13, 1992, pg. 1388-1391.		
		HECTOR SANCHEZ ET AL., "Thermal Management System for High Performance POWERPC Microprocessors", <i>IEEE CompCon 97</i> , San Jose, California, February 23-26, 1997, pg. 325-330, IEEE Computer Society Press, Los Alamitos, California.		
		PHILLIP E. ALLEN ET AL., "CMOS Analog Circuit Design", 1987, pg. 529-551, ISBN 0-03-006587-9, Holt, Rinehart and Winston, New York, New York.		
		ANALOG DEVICES, INC. brochure, "Low Power, Programmable Temperature Controller: TMP01", Rev. C., 1995, pg. 1-16.		

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number.²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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